



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of: Fu-Chieh Hsu

Application No.: 10/676,695

Filed: 9/30/2003

Art Unit.: 2815

Examiner: Allan R. Wilson

For: "METHOD OF FABRICATING A ONE TRANSISTOR FLOATING-BODY..."

**Docket No.: MST-013-1D**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: February 25, 2005

**AMENDMENT TRANSMITTAL**

1. Transmitted herewith is an amendment for this application.
2. **STATUS:** Applicant is other than a small entity.
3. **EXTENSION OF TERM:** A Petition And Fee For Extension Of Time (37.C.F.R. 1.136(a)) is filed herewith.
4. **FEE FOR CLAIMS:** The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated below:

	(Col.1)		(Col. 2)	(Col. 3)	LARGE ENTITY	
	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra	Rate	Addit. Fee
Total	21	Minus	20	= 1	x \$50 =	\$50.00
Indep.	2	Minus	3	= 0	x \$200 =	\$00.00
First Presentation of Multiple Dependent Claim					+ \$360 =	\$0
					Total Addit. Fee	\$50.00

5. **FEE PAYMENT and DEFICIENCY:** Credit card Form 2038 is attached authorizing the amount of \$350.00 to cover the cost of a one-month extension, additional claim fee and submission of Information Disclosure Statement. If additional fees are found to be due or overpayment has been made, please charge or credit Deposit Acct. 50-0574.

**SIGNATURE OF PRACTITIONER**

E. Eric Hoffman

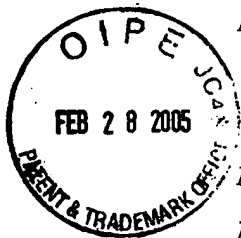
Reg. No. 38,186

Customer No. 022888

Tel.: (925) 895-3545

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

2/25/05      Carrie Reddick  
Date                      Signature: Carrie Reddick



Atty. Dkt. No.: MST-013-1D

SN: 10/676,695

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fu-Chieh Hsu

Assignee: Monolithic System Technology, Inc.

Title: Method of Fabricating A One Transistor Floating-  
Body DRAM Cell in Bulk CMOS Process With  
Electrically Isolated Charge Storage Region  
(as amended)

Serial No: 10/676,695 Filing Date: Sept. 30, 2003

Examiner: Allan Wilson Art Unit: 2815

Attorney Docket No.: MST-013-1D

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Date: February 25, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Sir:

Applicant submits the attached amendments and remarks  
in response to the Office Action dated October 28, 2004. A  
Request for Extension of Time, extending the period for  
response to February 28, 2005, accompanies the present  
response.

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